

Notice of References Cited	Application/Control No. 10/081,582	Applicant(s)/Patent Under Reexamination SHINOMIYA ET AL.	
	Examiner Michael B. Shingleton	Art Unit 2817	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-4,237,425	12/1980	Spiegel	330 / 273
	B	US-			
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	D	US-			
	E	US-			
	F	US-			
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	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N	JP362152203A	07/1987	Japan	Hirano et al.	
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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Giustolisi et al. "1.2-V CMOS op-amp with a dynamically biased output stage" IEEE Journal of Solid-State Circuits vol.35, Issue 4 April 2000 pp 632-636
	V	
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.